



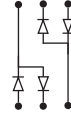
CMKD7000

**SURFACE MOUNT ULTRAmi™
DUAL PAIR OF
SERIES CONFIGURED
ULTRA-HIGH SPEED SILICON
SWITCHING DIODES**

ULTRAmi™



SOT-363 CASE



Central™ Semiconductor Corp.

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMKD7000 consists of two pair of electrically isolated, series configured ultra-high speed switching diodes in an ULTRAmi™ SOT-363 surface mount package, designed for ESD protection and high speed switching applications.

MARKING CODE: K00

MAXIMUM RATINGS: (T_A=25°C)

Peak Repetitive Reverse Voltage
Average Forward Current
Peak Forward Current
Power Dissipation
Operating and Storage Junction Temperature
Thermal Resistance

SYMBOL

V _R RM	100
I _O	200
I _{FM}	500
P _D	250
T _J , T _{stg}	-65 to +150
θ _{JA}	500

UNITS

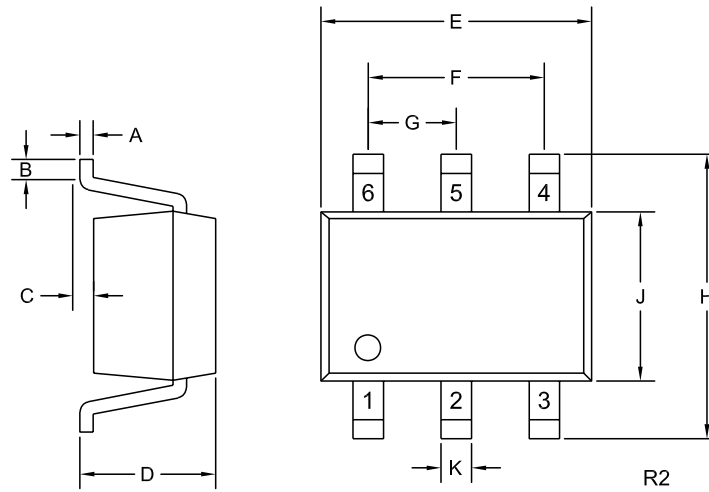
V
mA
mA
mW
°C
°C/W

ELECTRICAL CHARACTERISTICS PER DIODE: (T_A=25°C unless otherwise noted)

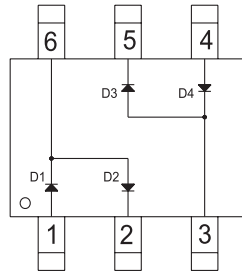
SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I _R	V _R =50V			300	nA
I _R	V _R =50V (T _A =125°C)			100	µA
I _R	V _R =100V			500	nA
BV _R	I _R =100µA	100			V
V _F	I _F =1.0mA	0.55		0.70	V
V _F	I _F =10mA	0.67		0.82	V
V _F	I _F =100mA	0.75		1.10	V
C _T	V _R =0, f=1.0MHz		1.5	2.6	pF
t _{rr}	I _R =I _F =10mA, R _L =100Ω, Rec. to 1.0mA		2.0	4.0	ns

**SURFACE MOUNT ULTRAmiTM
DUAL PAIR OF
SERIES CONFIGURED
ULTRA-HIGH SPEED SILICON
SWITCHING DIODES**

SOT-363 CASE - MECHANICAL OUTLINE



PIN CONFIGURATION



LEAD CODE:

- 1) ANODE D1
- 2) CATHODE D2
- 3) ANODE D3, CATHODE D4
- 4) ANODE D4
- 5) CATHODE D3
- 6) CATHODE D1, ANODE D2

MARKING CODE: K00

SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.004	0.010	0.10	0.25
B	0.005	-	0.12	-
C	0.000	0.004	0.00	0.10
D	0.031	0.043	0.80	1.10
E	0.071	0.087	1.80	2.20
F	0.051		1.30	
G	0.026		0.65	
H	0.075	0.091	1.90	2.30
J	0.043	0.055	1.10	1.40
K	0.006	0.012	0.15	0.30

SOT-363 (REV: R2)